## **ACKNOWLEDGEMENT RECEIPT**

## **Electronic Version**

Stylesheet Version v01

Title of Invention

EFS ID:

## WIRING STRUCTURE FOR INTEGRATED CIRCUIT WITH REDUCED INTRALEVEL CAPACITANCE

Submision Type: Utility Patent Filing

Application Number:

10/709204

59383

Server Response:



Confirmation Code	Message
ISVR1	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application
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First Named Applicant: Richard Wise
Attorney Docket Number: FIS920030028

Timestamp: 2004-04-21 11:30:11 EDT

From: us

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